

Benha University Benha Faculty of Engineering Date: 24/5/2017 Semester: 2nd Term 2016/2017

Examiner: Dr.Wael A. Mohamed Total Points: 90

Department: Electrical Program: General Time: 3 Hrs. Subject: Electrical Engineering and Circuit Analysis(b) Code: E1102 No. of Pages: 2



Model Answer

Question *O* (10 marks)

The switch in the circuit shown in Fig.Q1. has been open for a long time. At t = 0 the switch is closed. Find the expression for:

- a) v(t) when $t \ge \theta^+$
- b) i(t) when $t \ge 0$

Solution

a) The switch has been open for a long time, so the initial current in the inductor is 5 A, oriented from top to bottom. Immediately after the switch closes, the current still is 5 A, and therefore the initial voltage across the inductor becomes 20 - 5(1), or 15 V. The final value of the inductor voltage is 0 V. With the switch closed, the time constant is 80/1, or 80 ms. We use Eq. 7.60 to write the expression for v(t):

$$v(t) = 0 + (15 - 0)e^{-t/80 \times 10^{-3}}$$

= 15e^{-12.5t} V, $t \ge 0^+$.

b) We have already noted that the initial value of the inductor current is 5 A. After the switch has

Question @ (15 marks)

For the circuit shown in Fig.Q2, use the node voltage method to find the branch currents i_a , i_b and i_c .



Summing the currents away from node 1 yields

$$-10.6 + \frac{\mathbf{V}_1}{10} + \frac{\mathbf{V}_1 - \mathbf{V}_2}{1 + j2} = 0$$

Multiplying by 1 + j2 and collecting the coefficients of V_1 and V_2 generates the expression

$$\mathbf{V}_1(1.1 + j0.2) - \mathbf{V}_2 = 10.6 + j21.2$$



been closed for a long time, the inductor current reaches 20/1, or 20 A. The circuit time constant is 80 ms, so the expression for i(t) is

$$i(t) = 20 + (5 - 20)e^{-12.5t}$$

= (20 - 15e^{-12.5t}) A, $t \ge 0$.

We determine that the solutions for v(t) and i(t) agree by noting that

Fig.Q2 Summing the currents away from node 2 gives

$$\frac{\mathbf{V}_2 - \mathbf{V}_1}{1 + j2} + \frac{\mathbf{V}_2}{-j5} + \frac{\mathbf{V}_2 - 20\mathbf{I}_x}{5} = 0.$$

The controlling current \mathbf{I}_x is

$$\mathbf{I}_x = \frac{\mathbf{V}_1 - \mathbf{V}_2}{1 + j2}.$$

Substituting this expression for I_x into the node 2 equation, multiplying by 1 + j2, and collecting coefficients of V_1 and V_2 produces the equation

$$-5\mathbf{V}_1 + (4.8 + j0.6)\mathbf{V}_2 = 0.$$

The solutions for V_1 and V_2 are

$$\mathbf{V}_1 = 68.40 - j16.80 \text{ V}_2$$

 $\mathbf{V}_2 = 68 - j26 \text{ V}_2$

Hence the branch currents are

$$I_{a} = \frac{V_{1}}{10} = 6.84 - j1.68 \text{ A},$$

$$I_{x} = \frac{V_{1} - V_{2}}{1 + j2} = 3.76 + j1.68 \text{ A},$$

$$I_{b} = \frac{V_{2} - 20I_{x}}{5} = -1.44 - j11.92 \text{ A},$$

$$I_{c} = \frac{V_{2}}{-j5} = 5.2 + j13.6 \text{ A}.$$

Question 3 (15 marks)

Find the Thevenin equivalent circuit with respect to the terminals a,b for the circuit shown in Fig.Q3. Then find the maximum power that could be delivered to the impedance load connected to the terminals a,b.



$$(9+j4)\mathbf{I}_{a} - \mathbf{I}_{b} = -60/0^{\circ}$$

 $-\mathbf{I}_{a} + (9-j4)\mathbf{I}_{b} = 60/0^{\circ}$

Solving,

$$I_{a} = -5 + j2.5 \text{ A};$$
 $I_{b} = 5 + j2.5 \text{ A}$
 $V_{Th} = 4I_{a} + (4 - j4)I_{b} = 10/0^{\circ} \text{ V}$



Question @ (15 marks)

Two 480 V (rms) loads are connected in parallel. The two loads draw a total average power of 40,800 W at a power factor of 0.8 lagging. One of the loads draws 20 kVA at a power factor of 0.96 leading. What is the power factor of the other load?

 $S_{\rm T} = 40,800 + j30,600 \,\text{VA}$ $S_1 = 20,000(0.96 - j0.28) = 19,200 - j5600 \,\text{VA}$ $S_2 = S_{\rm T} - S_1 = 21,600 + j36,200 = 42,154.48/\underline{59.176^{\circ}} \,\text{VA}$ rf $= \sin(59.176^{\circ}) = 0.8587$

 $pf = cos(59.176^\circ) = 0.5124 lagging$

Question *© (*15 marks)

A three-phase positive sequence Y-connected source supplies $14 \, kVA$ with a power factor of 0.75 lagging to a parallel combination of a Y-connected load and a Δ -connected load. The Y-connected load uses 9 kVA at a power factor of 0.6 lagging and has an a-phase current of $10 \, \underline{/-30^{\circ}}$ A.

- a) Find the complex power per phase of the Δ -connected load.
- b) Find the magnitude of the line voltage.

[a]
$$S_{T\Delta} = 14,000/\underline{41.41^{\circ}} - 9000/\underline{53.13^{\circ}} = 5.5/\underline{22^{\circ}}$$
 kVA
 $S_{\Delta} = S_{T\Delta}/3 = 1833.46/\underline{22^{\circ}}$ VA
[b] $|\mathbf{V}_{an}| = \left|\frac{3000/\underline{53.13^{\circ}}}{10/\underline{-30^{\circ}}}\right| = 300$ V (rms)

$$|\mathbf{V}_{\text{line}}| = |\mathbf{V}_{\text{ab}}| = \sqrt{3}|\mathbf{V}_{\text{an}}| = 300\sqrt{3} = 519.62 \text{ V (rms)}$$

Question @ (20 marks) 2 marks for each point.

- (1) For the circuit shown in Fig.Q6-1, At $t = (0^{-})$, the circuit represents:
 - a) Natural response.
 - b) Step response.
 - c) None of the above.
- (2) For the circuit shown in Fig.Q6-1, If we replace the switch by a short circuit, the circuit represents an analysis type:
 - a) DC Analysis.
 - b) AC Analysis.
 - c) Transient Analysis.
- (3) For the circuit shown in Fig.Q6-1, If we replace the DC supply by an AC supply, the circuit represents an analysis type:
 - a) DC Analysis.
 - b) AC Analysis.
 - c) Transient Analysis.
- (4) If S = 40 + j 20, Which system has bigger value of Q_{added} , making the pf. =
 - a) 0.9 lead.
 - b) 0.9 lag.



Fig. Q6-1

(5) For the circuit shown in Fig.Q6-2, The output of the circuit behaves like :

- a) Low pass filter.
- b) High pass filter.
- c) Band pass filter.
- d) None of the above.

(6) For the circuit shown in Fig.Q6-3, the output of the circuit behaves like :

- a) Low pass filter.
- b) High pass filter.
- c) Band pass filter.
- d) None of the above.
- (7) Two factories consume the following power;

Factory (a) \Rightarrow S = 40 + j 20 Which Factory draws higher absolute value of current from the source?

- a) Factory (a) > Factory (b).
- b) Factory (b) > Factory (a).
- c) Factory (a) = Factory (b).
- d) None of the above.
- (8) Two loads have the following power values;
 - S1 = 40 + j 30

Find the total power if:

- a) The two loads are connected in series. $S_T = S_1 + S_2 = 60 + j50$
- b) The two loads are connected in parallel. $S_T = S_1 + S_2 = 60 + j50$
- (9) In AC circuits, when the load is pure resistive. The max power transferred to the load is found by the following equation:

S2 = 20 + j 20

- a) $(V_{th})^2 / 4R_{Th}$
- b) $(V_{th})^2 / 4R_L$
- c) $(V_{th})^2 / 4Z_{Th}$
- d) $(V_{th})^2 / 4Z_L$
- e) None of the above.

(10) For the circuit shown in Fig.Q6-4, the value of the resistor R_0 is selected to result in maximum power transfer to the 6 Ω load. The max. power transferred to the 6 Ω load can be calculated from the following equation. $R_0 = Zero$

- a) $(V)^2 / 4R_L$
- **b**) $(V)^2 / R_L$
- c) $(V)^2 / 4R_0$
- d) $(V)^2 / R_0$
- e) None of the above.













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Answer All Questions

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 S = 40 – j 20

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- a) $(V)^2 / 4R_L$
- b) $(V)^2 / R_L$
- c) $(V)^2 / 4R_0$
- d) $(V)^2 / R_0$
- e) None of the above.





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