

Answer the following questions:

Q1: (a) Find the values of I and V in the circuits shown in Fig. (1).

(b) Fig.(2) shows a simpler way to draw a transistor circuit. What are collector-emitter voltage and the transistor power dissipation?

Q2: (a) In fig.(3), $V_i = 20\sin(\omega t)$ V. Sketch the output waveform. Assume the diode is actual.

(b) The transistor parameters for the circuit in Figure (4) are $\beta_1 = 120$, $\beta_2 = 80$, $V_{BE1(on)} = V_{BE2(on)} = 0.7$ V. Determine the quiescent collector current in each transistor.

Q3: The 6.8V zener diode in the circuit of Fig. (5) is specified to have $V_z = 6.8$ V at $I_z = 5$ mA, $r_z = 20\Omega$, and $I_{zk} = 0.2$ mA. The supply voltage V^+ is nominally 10V but can vary by ± 1 V.

(a) Find V_0 with no load and with V^+ at its nominal value.

(b) Find the change in V_0 resulting from the ± 1 V change in V^+ .

(c) Find the change in V_0 when $R_L = 2$ k Ω .

(d) What is the minimum value of R_L for which the diode still operates in the breakdown region?

Q4: For the circuit shown in Fig. (6), draw a complete small-signal equivalent circuit utilizing an appropriate T- model for the BJT. Your circuit should show the values of all components, including the model parameters. Find R_{in} , R_o , the voltage gain (v_o/v_{sig}) and the current gain (i_o/i_i). Assume that the transistor $\beta = 100$.

Q5: The parameters of the circuit shown in Fig.(7), are $V^+ = 3.3$ V, $V^- = -3.3$ V, $R_B = 100$ k Ω , $R_E = 15$ K Ω , $R_c = 10$ K Ω , $R_L = 2$ K Ω , and $R_S = 2$ K Ω . The transistor parameters are $\beta = 120$, and $V_A = \infty$. Determine the dc emitter current I_E and V_{CE} . Find using hybrid- π -model the values of R_i , the output resistance R_o , the voltage gain (v_o/v_s), and the current gain (i_o/i_i).

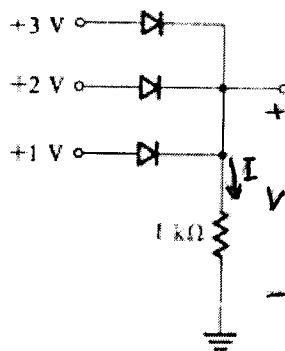


Fig.(1)

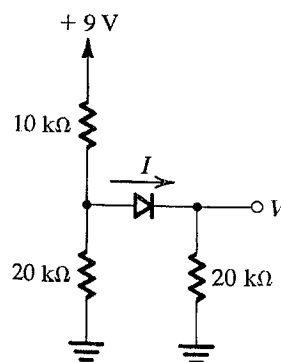
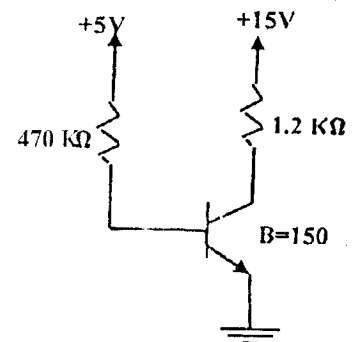


Fig.(2)



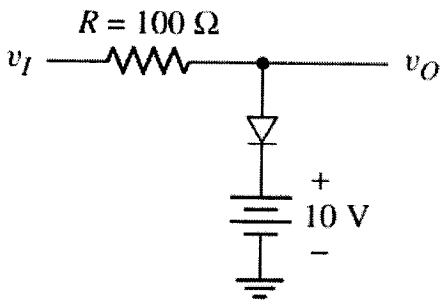


Fig.(3)

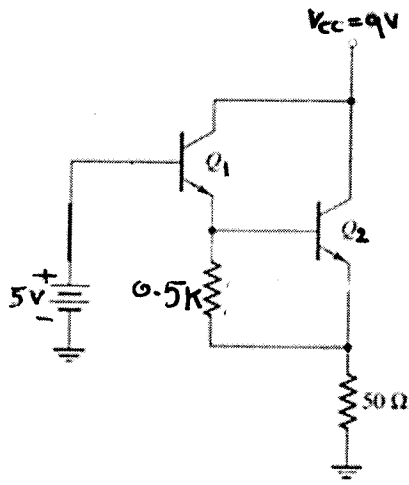


Fig.(4)

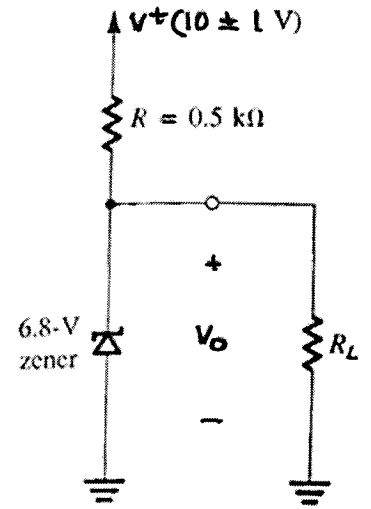


Fig.(5)

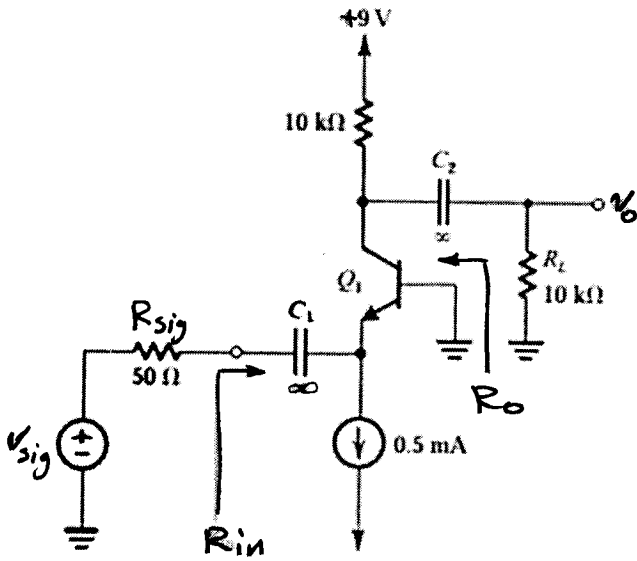


Fig.(6)

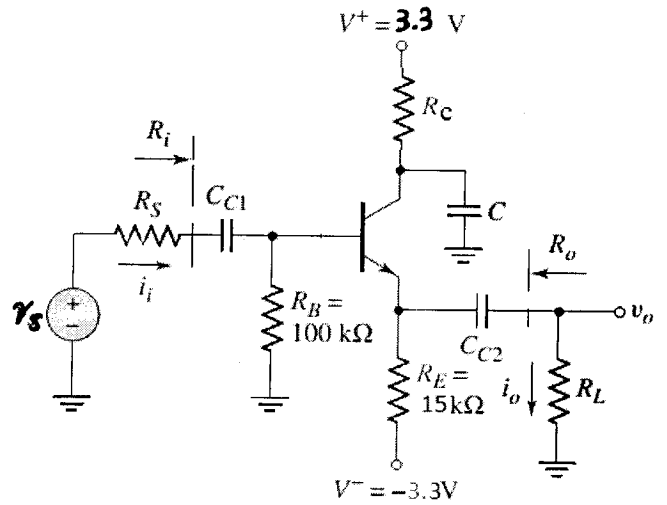


Fig.(7)

BEST WISHES

Hossam Labib

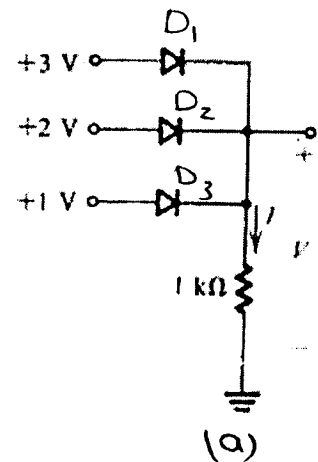
Q1 (a) Find the values of I and V in the circuits shown in Fig. (1).

For Fig. (a)

for D_1 on $V=3V$ D_2 and D_3 are off

$$\therefore V = 3V$$

$$I = \frac{V}{R} = \frac{3}{1k\Omega} = 3mA$$



For Fig. (b)

$$V_p = \frac{9 \times 20k}{10k + 20k} = 6V$$

Then diode is on and replaced by s.c

$$V = \frac{9 \times (20k \parallel 20k)}{10 + (20k \parallel 20k)}$$

$$= \frac{9 \times 10k}{20k} = 4.5V$$

$$I = \frac{V}{20k} = \frac{4.5}{20k} = 0.225mA$$

Another solution

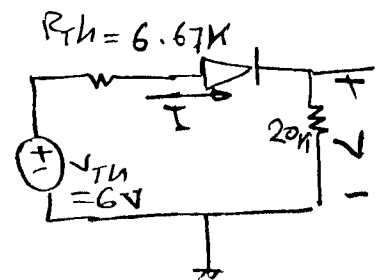
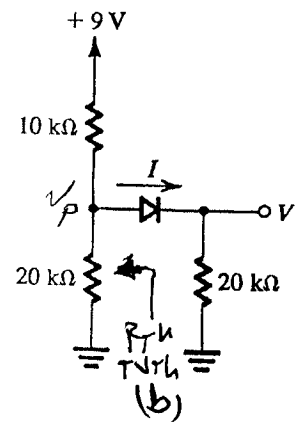
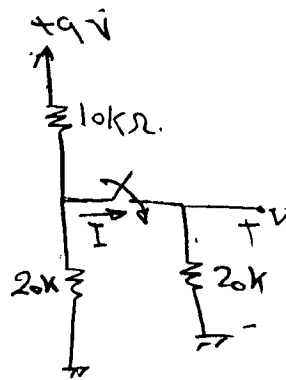
Using Thevenin the circuit becomes

$$V_{Th} = \frac{9 \times 20k}{10k + 20k} = 6V ; R_{Th} = 10k \parallel 20k = 6.67k$$

$\therefore V_p > V_n \Rightarrow$ diode on

$$\therefore V = \frac{6 \times 20k}{6.67k + 20k} = 4.5V$$

$$I = \frac{6}{6.67k + 20k} = 0.225mA$$



Q₁(b) (1 points)

Fig. (2) shows a simpler way to draw a Transistor circuit. What are collector emitter voltage and the Transistor power dissipation?

Solution

Let Transistor in Active Region

Loop (I)

$$5 = 470K I_B + V_{BE}$$

$$I_B = \frac{5 - 0.7}{470K} = 9.15 \mu A$$

$$\therefore I_C = \beta I_B = 150 * 9.15 \mu A = 1.3725 \text{ mA}$$

$$V_{CE} = V_{CC} - I_C R_C = 15 - 1.3725 * 1.2 = 13.353 \text{ V}$$

$$V_E = 0 \text{ V} \quad ; \quad \therefore V_{BE} = V_B - V_E$$

$$\therefore V_B = V_{BE} = 0.7 \text{ V} \quad ; \quad V_C = V_{CE} = 13.353 \text{ V}$$

$$\therefore V_B > V_E \Rightarrow \text{BE } \downarrow \text{ Forward bias}$$

$$\therefore V_B < V_C \Rightarrow \text{BC } \downarrow \text{ Reverse bias}$$

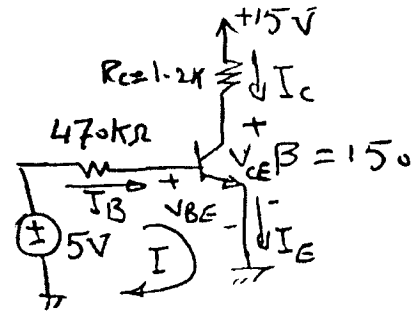
\therefore Transistor in Active Region

$$\therefore V_{CE} = 13.353 \text{ V} \quad ; \quad I_C = 1.3725 \text{ mA}$$

$$P_{diss} = I_{CQ} V_{CEQ}$$

$$= 1.3725 * 10^{-3} * 13.353 = 0.0183 \text{ W}$$

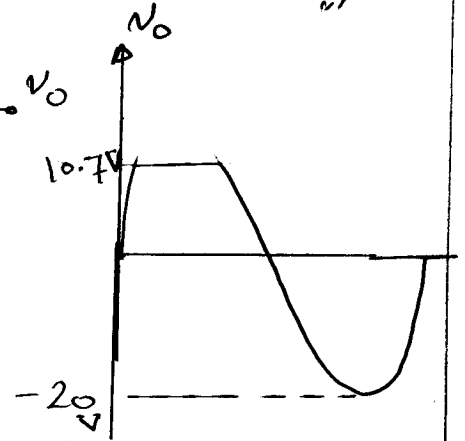
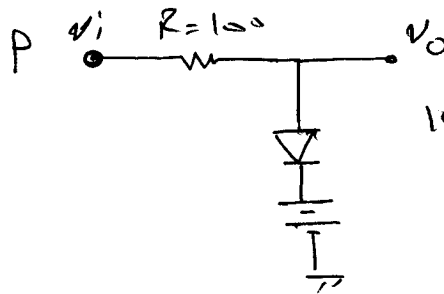
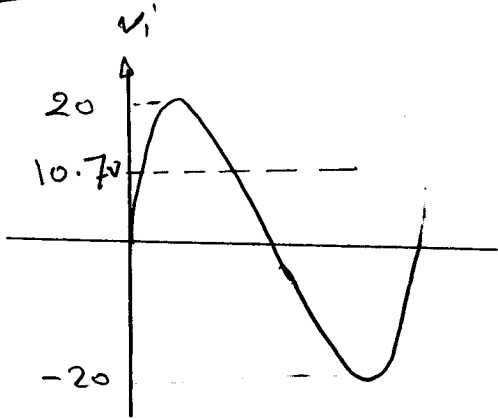
$$= 18.3 \text{ mW}$$



Q2: (r)

In Fig. (3) $v_i = 20 \sin(\omega t) \text{ V}$. Sketch the
 o/p waveform. Assume the diode is actual.

Solution



For +ve half cycle

for $v_i > 10.7 \Rightarrow$ diode is on

then $v_o = 10.7$

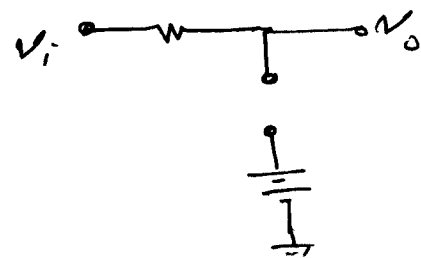
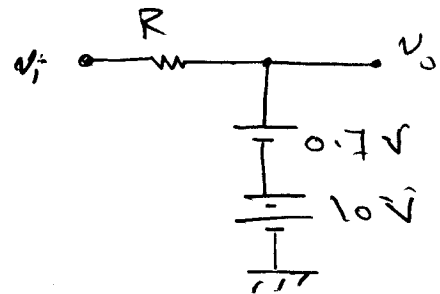
for $v_i < 10.7 \Rightarrow$ diode is off

$\therefore v_o = v_i$

for -ve half cycle

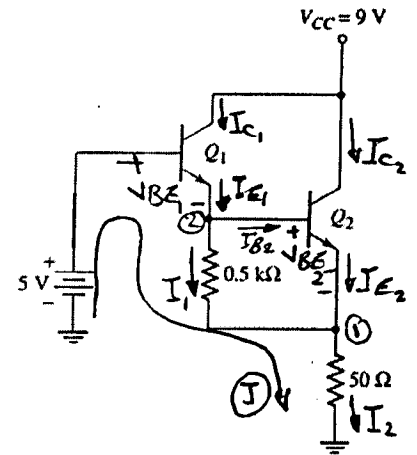
diode is off

$\therefore v_o = v_i$



Q2:

- (b) The transistor parameters for the circuit in Figure (2) are $\beta_1 = 120$, $\beta_2 = 80$, $V_{BE1(on)} = V_{BE2(on)} = 0.7$ V. Determine the quiescent collector current in each transistor.



Solution

Loop (J)

$$-5 + V_{BE1} + V_{BE2} + 50 * I_2 = 0$$

$$I_2 = \frac{5 - V_{BE1} - V_{BE2}}{50}$$

$$= \frac{5 - 0.7 - 0.7}{50} = \frac{3.6}{50} = 72 \mu\text{A}$$

$$I_1 = \frac{V_{BE2}}{0.5 \text{K}} = \frac{0.7}{0.5 \text{K}} = 1.4 \mu\text{A}$$

at node (1)

$$I_1 + I_{E2} = I_2 \Rightarrow I_{E2} = I_2 - I_1$$

$$I_{E2} = (72 - 1.4) \mu = 70.6 \mu\text{A}$$

$$I_{B2} = \frac{I_{E2}}{1 + \beta_2} = \frac{70.6 \mu}{81} = 871.605 \mu\text{A}$$

$$I_{C2} = \beta_2 I_{B2} = 871.605 \mu * 80 = 69.728 \mu\text{A}$$

at node (2)

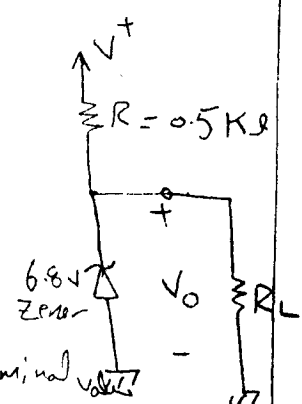
$$I_{E1} = I_{B2} + I_1 = 871.605 \mu + 1.4 \mu = 2.272 \mu\text{A}$$

$$I_{C1} = \alpha_1 I_{E1} = \frac{\beta_1}{1 + \beta_1} I_{E1}$$

$$= \frac{120}{121} * 2.272 \mu = 2.253 \mu\text{A}$$

Q3: - (12 points)

The 6.8V Zener diode in the circuit shown is specified to have $V_Z = 6.8V$ at $I_Z = 5mA$, $r_Z = 20\Omega$, and $I_{ZK} = 0.2mA$. The supply voltage V^+ is nominally 10V but can vary by $\pm 1V$.



(a) Find V_o with no load and with V^+ at its nominal value.

$$\therefore V_Z = V_{Z0} + r_Z I_Z$$

$$6.8 = V_{Z0} + 20 \times 5 \times 10^{-3}$$

$$\therefore V_{Z0} = 6.8 - 0.1 = 6.7V$$

with no load i.e. $R_L = \infty$

$$I = I_Z = \frac{V^+ - V_{Z0}}{R + r_Z}$$

$$= \frac{10 - 6.7}{0.5K + 20} = 6.35mA$$

$$\therefore V_o = V_{Z0} + I_Z r_Z$$

$$= 6.7 + 6.35 \times 20 = 6.83V$$

(b) Find the change in V_o resulting from the $\pm 1V$ change in V^+ . The change in V_o can be found from

$$\Delta V_o = \Delta V^+ \frac{r_Z}{R + r_Z}$$

$$= \pm 1 \times \frac{20}{0.5K + 20} = \pm 38.5mV$$

(c) Find the change in V_o when $R_L = 2K\Omega$

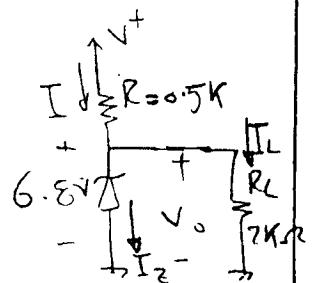
If $R_L = 2K\Omega$ is connected

$$\therefore I_L \approx \frac{V_Z}{R_L} \approx \frac{6.8}{2K} = 3.4mA$$

$$I = \frac{10 - 6.8}{0.5K} = 6.4mA$$

$$\therefore I = I_Z + I_L \Rightarrow I_Z |_{\text{Loaded}} = I - I_L =$$

$$I_Z |_{\text{Loaded}} = 6.4mA - 3.4mA = 3mA$$



Q3: (cont.)

change in Zener current $\Delta I_Z = I_Z|_{\text{Load}} - I_Z|_{\text{No-Load}}$
 $\Delta I_Z = 3 - 6.35 = -3.4 \text{ mA}$

\therefore change in $V_o \Rightarrow \Delta V_o = \Delta I_Z V_Z$
 $\Delta V_o = -3.4 \text{ mA} \times 20 = -68 \text{ mV}$

(d) what is the min. value of R_L for which the diode still operates in the breakdown region?

For the Zener at edge of the breakdown region then $I_Z = I_{ZK} = 0.2 \text{ mA}$ and $V_{Z0} \approx V_{ZK} \approx 6.7 \text{ V}$

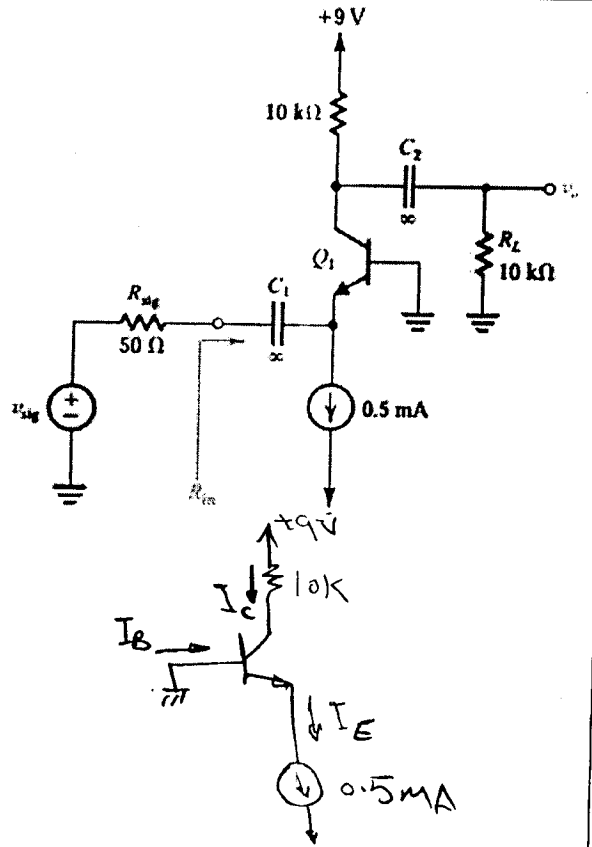
\therefore At this point the lowest current supplied through R is $I = \frac{V^+ - V_{ZK}}{R}$

$$= \frac{9 - 6.7}{0.5 \text{ k}} = 4.6 \text{ mA}$$

$$\therefore I_L = I - I_Z = 4.6 - 0.2 = 4.4 \text{ mA}$$

$$R_L|_{\text{min}} = \frac{V_o}{I_L} \approx \frac{V_Z}{I_L}$$
$$= \frac{6.7}{4.4 \text{ mA}} = 1.5 \text{ k}\Omega$$

Q4: For the circuit shown in Fig. (5), draw a complete small-signal equivalent circuit utilizing an appropriate T-model for the BJT (use $\alpha = 0.99$). Your circuit should show the values of all components, including the model parameters. Find R_{in} , R_o , the voltage gain (v_o/v_{sig}) and the current gain (i_o/i_i).



Solution

DC Analysis

- All capacitors are o.c
- reduce AC sources

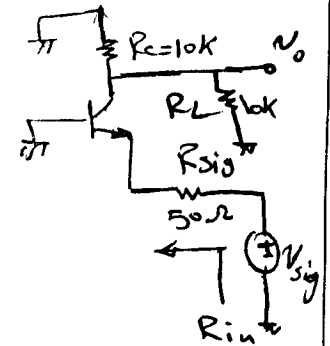
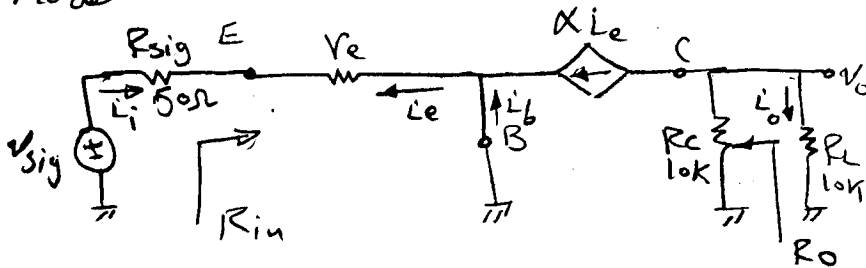
$$I_E = 0.5 \text{ mA}$$

$$r_e = \frac{V_T}{I_E} = \frac{25 \text{ mV}}{0.5 \text{ mA}} = 50 \Omega$$

AC Analysis

- All capacitors are s.c ; reduce DC sources i.e. v.s = s.c & c.s = o.c

Using T-model



$$R_{in} = r_e = 50 \Omega$$

$$R_o = R_c = 10 \text{ k}\Omega$$

$$v_{sig} = 0$$

$$v_o = -\alpha i_e (R_c \parallel R_L)$$

$$i_e = -i_i = \frac{-v_{sig}}{R_{sig} + r_e}$$

$$\therefore v_o = \frac{+\alpha v_{sig} (R_c \parallel R_L)}{R_{sig} + r_e}$$

$$\therefore A_v = \frac{v_o}{v_{sig}} = \frac{\alpha (R_c \parallel R_L)}{R_{sig} + r_e}$$

$$A_v = \frac{0.99 (10 \text{ k}\Omega \parallel 10 \text{ k}\Omega)}{50 + 50} = \frac{0.99 * 5 \text{ k}}{100} = 49.5 \text{ V/V}$$

Q4: (Cont.)

To find $A_i = \frac{i_o}{i_i}$

$$i_o = -\alpha i_e \frac{R_c}{R_c + R_L}$$

$$i_i = -i_e$$

$$\therefore A_i = \frac{i_o}{i_i} = \frac{\alpha R_c}{R_c + R_L}$$

$$= \frac{0.99 * 10k}{10k + 10k} = \frac{0.99}{2} = 0.495 \text{ A/A}$$

Q5: (15 points)

The parameters of the circuit shown are

$$V^+ = 3.3 \text{ V}, V^- = -3.3 \text{ V}, R_B = 100 \text{ k}\Omega$$

$$R_E = 15 \text{ k}\Omega, R_L = 2 \text{ k}\Omega, R_S = 2 \text{ k}\Omega,$$

and $R_C = 10 \text{ k}\Omega$. The transistor

parameters are $\beta = 120$ and

$$V_A = \infty. \text{ (a) Determine } I_{EQ} \text{ and } V_{CEQ}.$$

$$\text{(b) Find } R_i, R_o, A_v = \frac{V_o}{V_s}, \text{ and } A_L = \frac{V_o}{V_i}$$

Solution:-

DC Analysis

All capacitors are o.c and reduce AC sources

Loop ①

$$I_B R_B + V_{BE} + I_E R_E + \bar{V} = 0$$

$$\therefore I_B = \frac{I_E}{1+\beta}$$

$$\therefore \frac{I_E R_B}{1+\beta} + V_{BE} + I_E R_E + \bar{V} = 0$$

$$I_E \left(\frac{R_B}{1+\beta} + R_E \right) = -\bar{V} - V_{BE}$$

$$I_{EQ} = \frac{-\bar{V} - V_{BE}}{R_E + \frac{R_B}{1+\beta}} = \frac{3.3 - 0.7}{15 \text{ k} + \frac{100 \text{ k}}{121}} = 0.164 \text{ mA}$$

$$V_E = I_E R_E + \bar{V} = 0.164 \times 15 - 3.3 \text{ V} = -0.84 \text{ V}$$

$$V_{CEQ} = V_C - V_E \quad ; \quad V_C = V^+ - I_C R_C = 1.674 \text{ V}$$

$$V_{CEQ} = 1.674 - (-0.84) = 2.514 \text{ V}$$

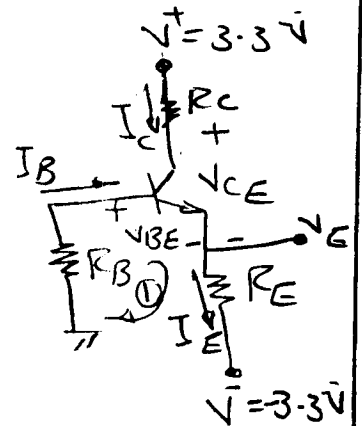
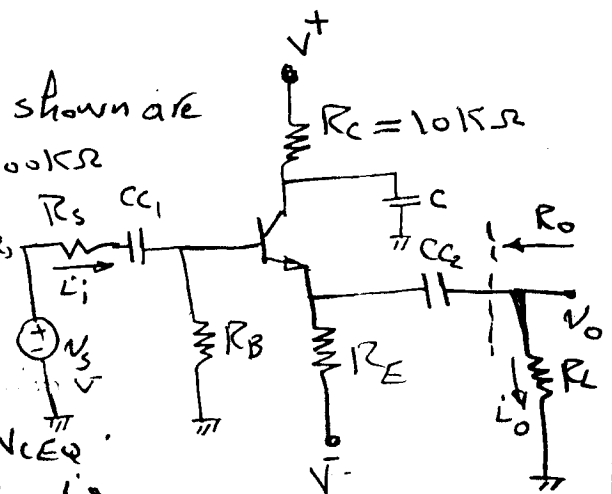
$$I_C = \beta I_E = \left(\frac{\beta}{1+\beta} \right) I_E = \frac{120}{121} \times 0.164 \text{ mA} = 0.1626 \text{ mA}$$

$$g_m = \frac{I_C}{V_T} = \frac{0.1626 \text{ mA}}{25 \text{ mV}} = 6.504 \text{ mA/V}$$

$$r_e = \frac{V_T}{I_E} = \frac{25 \text{ mV}}{0.164 \text{ mA}} = 152.44 \Omega$$

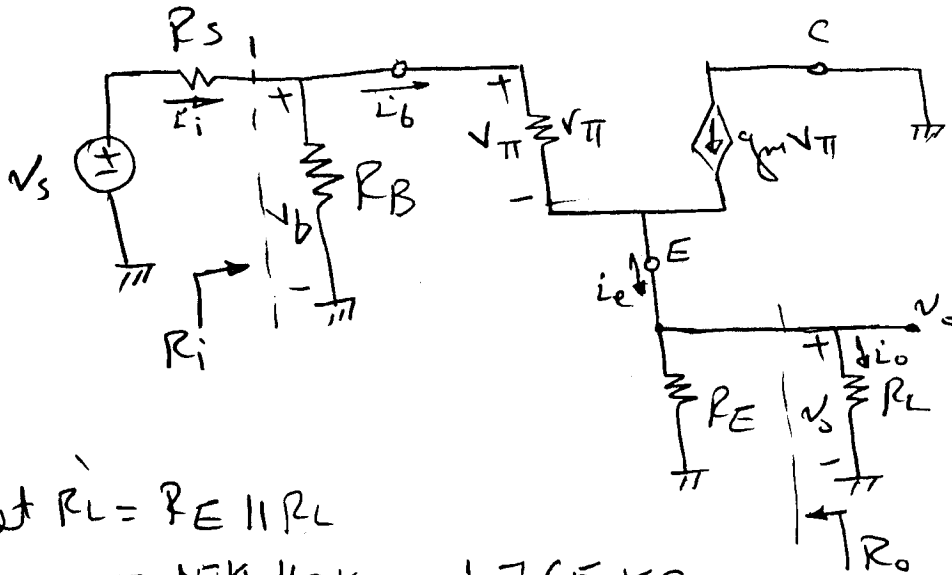
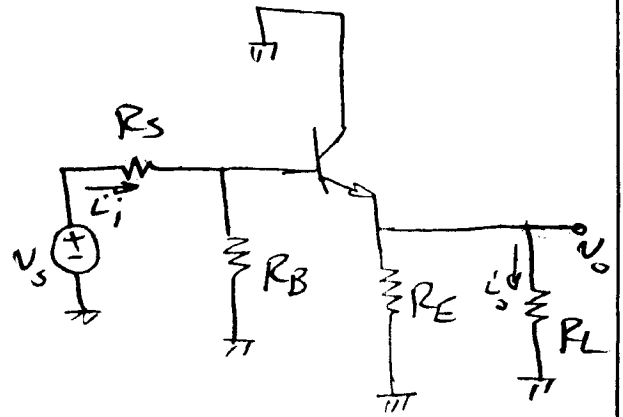
$$\therefore V_A = \infty \Rightarrow r_o = \infty$$

$$r_{\pi} = \frac{\beta}{g_m} = \frac{120}{6.504 \text{ mA/V}} = 18.45 \text{ k}\Omega.$$



AC Analysis

- All capacitors are s.c
- Reduce DC sources using π -model



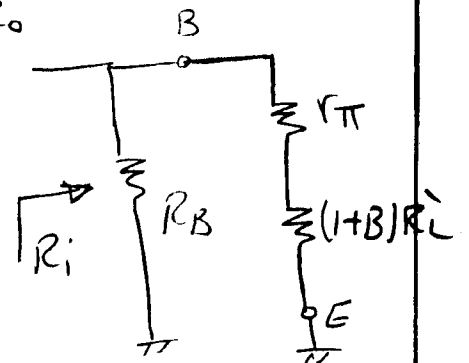
Let $R_L' = R_E \parallel R_L$
 $= 15K \parallel 2K = 1.765 K\Omega$

To find R_i

$$R_i = R_B \parallel [r_{\pi} + (1 + \beta) R_L']$$

$$= 100K \parallel [18.45K + (121) * 1.765K]$$

$$= 100K \parallel (232.015K) = 69.88 K\Omega$$

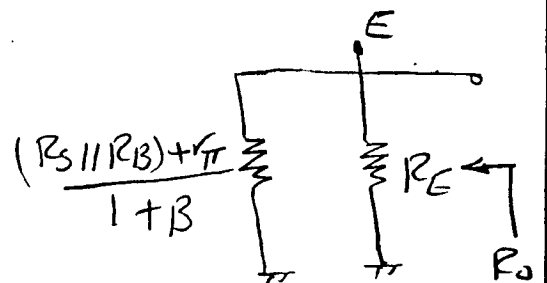


To find R_o

$$R_o|_{v_s=0} = R_E \parallel \left[\frac{(R_s \parallel R_B) + r_{\pi}}{1 + \beta} \right]$$

$$= 15K \parallel \left[\frac{(2K \parallel 100K) + 18.45K}{121} \right]$$

$$= 15K \parallel 168.68 = 166.8 \Omega$$



To find $A_v = \frac{V_o}{V_s}$

$$V_b = V_s \frac{R_i}{R_s + R_i}$$

$$V_o = V_b \frac{(1+\beta)R_L}{r_{\pi} + (1+\beta)R_L}$$

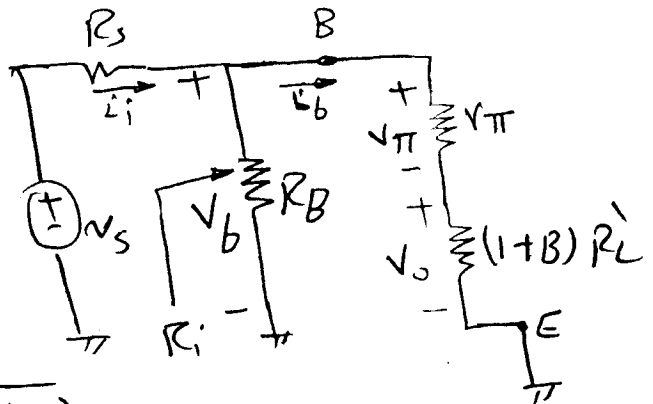
$$\therefore V_o = V_s \frac{R_i}{R_s + R_i} * \frac{(1+\beta)R_L}{r_{\pi} + (1+\beta)R_L}$$

$$\therefore A_v = \frac{V_o}{V_s} = \frac{R_i}{R_s + R_i} * \frac{(1+\beta)R_L}{r_{\pi} + (1+\beta)R_L}$$

$$= \frac{69.88k}{2k + 69.88k} * \frac{121 * 1.765k}{18.45k\Omega + 121 * 1.765k}$$

$$= 0.9722 * 0.9205$$

$$= 0.895 \quad V/V$$



To find $A_i = \frac{I_o}{I_i}$

$$I_o = I_e \frac{R_E}{R_E + R_L}$$

$$I_e = (1+\beta)I_b$$

$$I_b = I_i \frac{R_B}{R_B + r_{\pi} + (1+\beta)R_L}$$

$$\therefore I_o = (1+\beta)I_i \frac{R_B}{R_B + r_{\pi} + (1+\beta)R_L} \frac{R_E}{R_E + R_L}$$

$$\therefore A_i = \frac{I_o}{I_i} = (1+\beta) \frac{R_B}{R_B + r_{\pi} + (1+\beta)R_L} * \frac{R_E}{R_E + R_L}$$

$$= (1+120) \frac{100k}{100k + 18.45k + 121 * 1.765k} * \frac{15k}{15k + 2k}$$

$$= 121 * 0.3012 * 0.8824$$

$$= 32.17 \quad A/A$$

