

Electrical Department B. Sc. Course Exam



Power Electronics (A), E1335 Allowed Time: 3 Hours.

Question **(**25 marks)

- (a) Mention (Only) the suitable power converter in order to match the following I/O relationship.
 - a. Dc Chopper
 - b. Inverter
 - c. Ac Voltage Controller
 - d. Rectifier
 - e. Dc Chopper
 - f. Inverter

(b) Draw the characteristic Curves of: SCR – TRIAC – DIAC – BJT – Shokley Diode.



SCR



DIAC

BJT

Shokley Diode

TRIAC



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- (c) Draw the transistor model of the thyristor and state the theory of operation to pass current from anode to cathode. And state the turning ON conditions.
 - 1- By applying positive voltage at gate between gate and cathode
 - Q2 will forward biased and turn on
 - 2- When Q2 is saturated or On the current flow from collector 2 to emitter 2
 - Q1 starts to conduct V_{E1B1}greater than 0 forward biased and current flow from E1 to C1
 - 4- When gate current interrupted thyristor still in on state

Turning ON conditions:

- 1- Positive anode cathode voltage
- 2- Gate pulse
- 3- Anode current greater than latching current
- (d) The Power transistor is used as a switch and it has the following parameters $V_{CC}=200 \text{ V}$, $V_{BE(sat)}=3 \text{ V}$, $I_B=8 \text{ A}$, $V_{CS(sat)}=2 \text{ V}$, $I_C=100 \text{ A}$, $t_d=0.5 \text{ }\mu\text{s}$, $t_r=1 \text{ }\mu\text{s}$, $t_s=5 \text{ }\mu\text{s}$, $t_f=3 \text{ }\mu\text{s}$ and $f_s=10 \text{ KHz}$. The duty cycle is k = 50 %. The collector-to-emitter leakage current is $I_{CEO}=3 \text{ mA}$ Determine the power loss due to collector current during (a) turn on $t_{on}=t_d+t_r$, (b) conduction period t_n , (c) turn off $t_{off}=t_s+t_f$. (d) off-time t_0 , And (e) total average power losses p_T . (d) Plot the instantaneous power due to collector current $P_c(t)$.





Solution

 $T = 1/f_s = 100 \ \mu s, \ k = 0.5, \ kT = t_d + t_r + t_n = 50 \ \mu s, \ t_n = 50 - 0.5 - 1 = 48.5 \ \mu s, \ (1 - k)T = t_s + t_f + t_o = 50 \ \mu s, \ and \ t_o = 50 - 5 - 3 = 42 \ \mu s.$

a. During delay time, $0 \le t \le t_d$:

 $i_c(t) = I_{CEO}$ $v_{CE}(t) = V_{CC}$

The instantaneous power-due to the collector current is

$$l_c(t) = i_c v_{CE} = I_{CEO} V_{CC}$$

= 3 × 10⁻³ × 250 = 0.75 W

The average power loss during the delay time is

$$P_d = \frac{1}{T} \int_0^{t_d} P_c(t) dt = I_{CEO} V_{CC} t_d f_s$$

= 3 × 10⁻³ × 250 × 0.5 × 10⁻⁶ × 10 × 10³ = 3.75 mW

During rise time, $0 \le t \le t_r$:

$$i_{c}(t) = \frac{I_{CS}}{t_{r}} t$$
$$v_{CE}(t) = V_{CC} + (V_{CE(sat)} - V_{CC})\frac{t}{t_{c}}$$

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$$P_r = \frac{1}{T} \int_0^{t_r} P_c(t) dt = f_s I_{CS} t_r \left[\frac{V_{CC}}{2} + \frac{V_{CE(\text{sat})} - V_{CC}}{3} \right]$$

= 10 × 10³ × 100 × 1 × 10⁻⁶ $\left[\frac{250}{2} + \frac{2 - 250}{3} \right]$ = 42.33 W

The total power loss during the turn-on is

 $P_{\rm on} = P_d + P_r$ = 0.00375 + 42.33 = 42.33 W

b. The conduction period. $0 \le t \le t_n$:

$$i_{c}(t) = I_{CS}$$

$$v_{CL}(t) = V_{CE(sat)}$$

$$P_{c}(t) = i_{c}v_{CE} = V_{CE(sat)}I_{CS}$$

$$= 2 \times 100 = 200 \text{ W}$$

$$P_{n} = \frac{1}{T} \int_{0}^{t_{c}} P_{c}(t)dt = V_{CE(sat)}I_{CS}t_{n}f_{s}$$

$$= 2 \times 100 \times 48.5 \times 10^{-6} \times 10 \times 10^{3} = 97 \text{ W}$$

c. The storage period. $0 \le t \le t_s$:

(4.2

$$i_{c}(t) = I_{CS}$$

$$v_{CE}(t) = V_{CE(sat)}$$

$$P_{c}(t) = i_{c}v_{CL} = V_{CE(sat)}I_{CS}$$

$$= 2 \times 100 = 200 \text{ W}$$

$$P_{s} = \frac{1}{T} \int_{0}^{t_{s}} P_{c}(t)dt = V_{CE(sat)}I_{CS}t_{s}f_{s}$$

$$= 2 \times 100 \times 5 \times 10^{-6} \times 10 \times 10^{3} = 10 \text{ W}$$

The fall time, $0 \le t \le t_f$:

$$i_{c}(t) = I_{CS}\left(1 - \frac{t}{t_{f}}\right), \text{ neglecting } I_{CEO}$$
$$v_{CE}(t) = \frac{V_{CC}}{t_{f}} t, \text{ neglecting } I_{CEO}$$
$$P_{c}(t) = i_{c}v_{CE} = V_{CC}I_{CS}\left[\left(1 - \frac{t}{t_{f}}\right)\frac{t}{t_{f}}\right]$$

This power loss during fall time is maximum when $t = t_f/2 = 1.5 \,\mu s$ gives the peak power.

$$P_m = \frac{V_{CC}I_{CS}}{4}$$

= 250 × $\frac{100}{4}$ = 6250 W



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$$P_{c}(t) = i_{c} v_{CE} = I_{CS} \frac{t}{t_{r}} \left[V_{CC} + (V_{CE (sat)} - V_{CC}) \frac{t}{t_{r}} \right]$$

The power $P_c(t)$ is maximum when $t = t_m$, where

$$t_m = \frac{t_r V_{CC}}{2[V_{CC} - V_{CE \text{ (sat)}}]}$$
$$= 1 \times \frac{250}{2(250 - 2)} = 0.504 \text{ }\mu\text{s}$$

and Eq. (4.22) yields the peak power

$$P_{i} = \frac{V_{CC}^{2}I_{CS}}{4[V_{CC} - V_{CE(sat)}]}$$
$$= 250^{2} \times \frac{100}{4(250 - 2)} = 6300 \text{ V},$$

 $P_{f} = \frac{1}{T} \int_{0}^{t_{f}} P_{c}(t) dt = \frac{V_{CC} I_{CS} t_{f} f_{s}}{6}$ $= \frac{250 \times 100 \times 3 \times 10^{-6} \times 10 \times 10^{3}}{6} = 125 \text{ W}$ (4.31)

The power loss during turn-off is

$$P_{\text{off}} = P_s + P_f = I_{CS} f_s \left(t_s V_{CE(\text{sat})} + \frac{V_{CC} t_f}{6} \right)$$

$$= 10 + 125 = 135 \text{ W}$$
(4.32)

d. Off-period, $0 \le t \le t_0$:

$$i_{c}(t) = I_{CEO}$$

$$v_{CE}(t) = V_{CC}$$

$$P_{c}(t) = i_{c}v_{CE} = I_{CEO}V_{CC}$$

$$= 3 \times 10^{-3} \times 250 = 0.75 \text{ W}$$

$$P_{0} = \frac{1}{T} \int_{0}^{I_{o}} P_{c}(t)dt^{*} = I_{CEO}V_{CC}t_{o}f_{s}$$

$$= 3 \times 10^{-3} \times 250 \times 42 \times 10^{-6} \times 10 \times 10^{3} = 0.315 \text{ W}$$
(4.33)

e. The total power loss in the transistor due to collector current is

$$P_T = P_{on} + P_n + P_{off} + P_0$$

= 42.33 + 97 + 135 + 0.315 = 274.65 W (4.3)

f. The plot of the instantaneous power is shown in Figure 4.12





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Question 2 (20 marks)

(a) The battery voltage in the Figure is E= 12 V and its capacity is 100Wh. The average charging current should be $I_{dc}= 5$ A. The primary input voltage is $V_p= 120$ V, 60 Hz. Calculate (a) the



conduction angle δ Of the diode, (b) the current-limiting resistance R, (c) the power rating P_R of R, (d) the charging time h_o in hours, (e) the rectifier efficiency η , and (f) the PIV of the diode.

Solution

 $E = 12 \text{ V}, V_p = 120 \text{ V}, V_s = V_p/n = 120/2 = 60 \text{ V}, \text{ and } V_m = \sqrt{2} V_s = \sqrt{2} \times 60 = 84.85 \text{ V}.$

- **a.** From Eq. (3.17), $\alpha = \sin^{-1}(12/84.85) = 8.13^{\circ}$ or 0.1419 rad. $\beta = 180 8.13 = 171.87^{\circ}$. The conduction angle is $\delta = \beta - \alpha = 171.87 - 8.13 = 163.74^{\circ}$.
- **b.** The average charging current I_{dc} is

$$I_{dc} = \frac{1}{2\pi} \int_{\alpha}^{\beta} \frac{V_m \sin \omega t - E}{R} d(\omega t)$$
$$= \frac{1}{2\pi R} (2V_{p_1} \cos \alpha + 2E\alpha - \pi E), \text{ for } \beta = \pi - \alpha$$
(3.18)

which gives

$$R = \frac{1}{2\pi I_{dc}} \left(2V_m \cos \alpha + 2E\alpha - \pi E \right)$$

= $\frac{1}{2\pi \times 5} \left(2 \times 84.85 \times \cos 8.13^\circ + 2 \times 12 \times 0.1419 - \pi \times 12 \right) = 4.26 \Omega$

c. The rms battery current $I_{\rm rms}$ is

$$I_{\rm rms}^2 = \frac{1}{2\pi} \int_{\alpha}^{\beta} \frac{(V_m \sin \omega t - E)^2}{R^2} d(\omega t)$$

= $\frac{1}{2\pi R^2} \left[\left(\frac{V_m^2}{2} + E^2 \right) (\pi - 2\alpha) + \frac{V_m^2}{2} \sin 2\alpha - 4V_m E \cos \alpha \right]$
= 67.4 (3.19)

or $I_{\rm rms} = \sqrt{67.4} = 8.2$ A. The power rating of R is $P_R = 8.2^2 \times 4.26 = 286.4$ W.

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- (b) A Full-wave controlled bridge rectifier has an ac input voltage of 220 V rms at 50 Hz and a 100 Ω load resistor. The firing angle is 600. Draw the power circuit and then calculate the following.
 - a) Average load voltage b) Average load current
 - c) Power absorbed by the load d) Converter power factor
 - e) Draw the wave form of the following items:

Ac supply, Firing angle, load voltage, load current, ac supply current, power devices voltage drop, and power devices currents



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Vin = 220 Vins F= 50 HZ R= 100 JL &= 60° $\frac{V_0}{\alpha v_R} = \frac{V_m}{\pi} \left(1 + (os \,\alpha) \right) = \frac{220 * \sqrt{2}}{3 \cdot 14} \left(1 + (os \, 6o) \right) = 148.55 \,V$ $\frac{2_{00NR}}{R} = \frac{V_{00NR}}{R} = \frac{143.55}{100} = 1.4855 A$ BX $\frac{C}{X} = \frac{V_{m}}{Rms} \int \frac{1 - \frac{\alpha}{T} + \frac{Sin2\alpha}{TT}}{\sqrt{TT}} = \frac{220 \times \sqrt{2}}{\sqrt{2}} \int \frac{1 - \frac{60}{180} + \frac{Sin120}{3.14}}{3.14}$ = 213.56 V 20 = 2-1356 A Pabsorbed = 20 XR = (2-1356) × 100 = 456.088 W DX PF= P = Vorpons 20 pms = 0.97 B. Via irons



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AT3 SR e: Wave Form Vin E Vilge 37 > wt 21 A Firing TIOTZ sut 450 450 Fining w TITA 225° Vo 22052 wt A sц 377 20 22012/100 owt i supply ! 22052/100 wt UT1 wt 251 2005/100



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- (c) What the drawbacks of the single phase half-wave uncontrolled rectifier?
 - 1- Average value of load voltage is low
 - 2- High ripple in load voltage and load current
 - 3- Need large filter and high cost
 - 4- High weight

Question 3 (15 marks)

(a) Draw the power circuit of Dual Converter and mention (only) modes of operation.

Converter. Single phase Doual Circuitating Current Inductors Th 12 3115 0 5 a D T2 TI T4 Tz 4 Converter (2) Converter (1) operation Modes or Converter mode (one converter only) Rectifier (1) converter as inverter & Converter Mode (2)Invertering as rectifier Circuitating mode Current

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Power Electronics (A), E1335 Allowed Time: 3 Hours.

(b) Draw the Power circuit of the series converter and explain its operation with waveform.





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0 < 9 < 180 OKNOK 2 Vm $V_{O_1} = \frac{V_m}{\pi} \left(1 + (O_S \alpha_1) \right)$ 0 < Voz < 2 Vm Noz= Vm (1+ Cosdz V2 = V0, + V02 $= \frac{V_m}{\pi} \left(1 + \cos \alpha_1 + 1 + \cos \alpha_2 \right)$ $= \frac{Vm}{\pi} \left(2 + \cos q_1 + \cos q_2 \right)$ autput range of series Goverler $if \quad x = x_{1} = 0$ $i \quad 0 < V_{0} < \frac{2}{N}$ $\alpha_1 = 0 \quad \text{or} \quad \varphi_2 < \pi \qquad \qquad V_0 = \frac{V_n}{\pi} \left(3 + 6 \right) \quad \alpha_2$ if 2Vm < Vo < 4Vm



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* Single phase parallel Converter. This Converter is Used to Share Load Current. IOI --- IO = IO, + IO, 12 KTI 3118 4T4 本 T3 -> Joz Rload 长丁 Tr 岱 Tai φT3 10

(c) Draw the Power circuit of the parallel Converter



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(d) Explain the two firing techniques for thyristor.

Firing Techniques Thyristor Linear Firing Circuit Π Firing Circuit-Cosine [2] Circuit. Fining linear TI Vc ---- Control voltage VR --- Refrence voltage. NA Vc wt 29 wT T x 2下 Tt q at when vct at when vct -- propotional metation. VCXX .. $\therefore \alpha = k V_{c}$



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2Vm GSQ DE .. V From (1) in (2) - + KVC ° V_{de} = 2 Vm Cos (K Ve) → This eq " relation Between Vdc and Vc Not linear. Cosine Jechnique. 2 VC. ١ 24 Noc & Cosx $\cos q = k V c$ $x = \cos^{-1}(k V c)$ - Vdc = 2Vm Cos (cos KVc)



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Power Electronics (A), E1335 Allowed Time: 3 Hours.

Question 4 (15 marks)

(a) Draw the Power Circuit and wave form of three phase Full wave Uncontrolled Rectifier.

* Three phase full wave Un controlled bridge pectifier. De = ID-ID4 1ID4 5 B B C YO P C DG D4 D. 3- \$ converte For D1 & D3, D5 will be Forward if its anode voltage is largest the voltage. For D2 D4 D6 will be Forward if its cathode voltage is largest -ve voltage. Only two diodes are on



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Power Electronics (A), E1335 Allowed Time: 3 Hours.

voltage 90 Vm (~ Cos (wt+30 30 313 (- cos 120 + cos 60 = 3/3 Vm = 3 13 Vm (+ + + π 316 Vs = 2-34 Vs ; Vs --- Phase voltage. Ξ voltage (v3) Vm2 Sin2 (wt+ 30) dwt 6 21 RMS 90 3 Vm (1- 652(20t+30) dwt 211 30 90 (we - Sin 2 (wt + 30 3 Vm = 1.655 Vm = 2-341 Vs ---- Vs phase volta

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Power Electronics (A), E1335 ^a Allowed Time: 3 Hours.

- (b) A three pulse uncontrolled rectifier is connected to a $3-\phi$, 4-wire, 220 V AC source. If the load resistive is 20 Ω , find
 - a. The maximum load voltage.
 - c. The average load current.
 - e. The maximum diode current.
 - g. The average diode current.
 - i. The pulse number.
- Solution a) The maximum value of line voltage is

 $V_{\rm L(m)} = \sqrt{2} (220) = 311 \, \rm V$

The maximum value of the phase voltage is

$$V_{\rm m} = 311 / \sqrt{3} = 179.6 \, {\rm V}$$

- b) $V_{o(avg.)} = 0.827 * 179.6 = 148.5 \text{ V}$
- c) $I_{o(avg.)} = V_{o(avg.)}/R = 148.5/5.20 = 7.4$ A
- d) $I_{o(m)} = V_m/R = 179.6/20 = 9 \text{ A}$
- e) $I_{D(m)} = I_{o(max)} = 9$ A
- f) PIV $\geq V_{L(m)} = 311 \text{ V}$

- b. The average load voltage.
- d. The maximum load current.
- f. The PIV of the diode.
- h. The form factor.
- J. The conduction angle.

g)
$$I_{D(avg.)} = \frac{I_{o(avg.)}}{3} = 7.4/3 = 2.5 \text{ A}$$

h) FF = $\sqrt{n} = \sqrt{3} = 1.732$
i) $P = 3$
j) $\theta = 120^{\circ}$

Question 5 (15 marks)

(*a*) Draw the power circuit of Buck Converter and Waveform of inductor current , load current , diode current , load voltage and capacitor current





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(b) Prove that, $L_{\min} = \frac{R(1-D)}{2F_{swt.}}$, The boundary of continuous condition is when

 $I_{min}=0$. If the value of $I_{min}<0$, the converter enters in the discontinuous conduction mode.

endition Between Continaus and Baundry dis Continaus Load Current Imax Imi ton max+. Imin OUVR avr DIL 0.0 Dt dun OFE time 0 Imin Imax st. toff mix 30 Lmax AVR



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O, O
D - Imax, Imin _ VoarR
2 2 R
Imax + Imin = 2 VOANR
Parent P
2 D I max - Imin = Yoark Loff
2 Imax = VOAVR 2, toff
R'L
- is Imax - Vaarp & atoff 7
R 22 P
(1)-(2) 30
() Zmax, Emin - 2 VoarR
R
@ Imax_Imin _ Voare toff
L
2 Imin = 240aVR - VoavR toff/1
NOTESR
Emin = VoarR toff P



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lon T 7- Toff Toff T Toff 3 From (1) (1) 11-Imax = VOAVR R 21 1 The Controller Curre max In Condi = at Baundry I min = 0 كرات (1 - D)NOAR Imin 22 R T (1-1 0 21 R



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1-D) R 2) K T min Criti Cal nin Swt antinaus >7 nin dis Continaus Lmin 25



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(c) For Buck Converter, Drive ripple voltage Relation
$$\frac{\Delta v_o}{v_o} = \frac{1}{8CLF_{swt.}^2}(1-D)$$





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SO AIL 80 Swt 10 DIL 5 0 7 C Imax + Imin IarR 2 Ver Yoo Ic Peak Imax Imax Imin 2 2 de Ve . . = C e + ve 50 = C ===========

Good Luck, Mohamed Awaad