

Benha University

Benha Faculty of Engineering

Electrical Engineering and Circuit Analysis(b) (E1102)

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2nd Term 2014 تخلفات

1st Year Electrical

Time: 3 Hrs



≹10Ω v₀

9 kΩ

≶1 kΩ

Exam with Model Answer

Question (1): [10 Marks]

In the circuit in Fig.1, the switch has been closed for a long time before opening at t=0. Write the expression for $v_o(t)$ and find v_o at t=1ms. 30 mA (L = 14.43 mH)

Solution:

The switch was closed for very long time and the inductor was a short circuit and it the inductor current equals $i_L(0^-) = i_L(0^+) = 30mA * 1K\Omega / (1K\Omega + 9K\Omega) = 3 mA$

After the switch is opened:

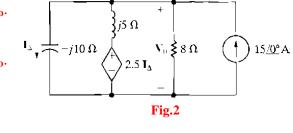
The inductor will dissipate its energy into the 10Ω resistor as follows,

initial $i_L = 3mA$, $\tau = L/R = 14.43 \text{ mH} / 10 \Omega = 1.443 \text{ mSec.}$ Final $i_L = 0$, $i_L(t) = 3 e^{-t/\tau} mA$ $v_L = v_R = v_o(t) = -30 e^{-t/\tau} mV$ $v_{o}(1ms) = -15 mV$

Question (2): [15 Marks]

For the circuit shown in Fig.2,

- a) Write the node voltage equations needed to find the voltage V_0 . (write both the main equations and the auxiliary equations).
- b) Write the mesh current equations needed to find the voltage V_0 . (write both the main equations and the auxiliary equations).
- c) Which way is preferred and why?



Solution:

- a) Write the node equation (only one) with the auxiliary equation of the dependent source control I_{Λ} .
- b) Write the two mesh equations (the third mesh is known with 15A value) with the auxiliary equation of the dependent source control I_{Λ} .
- c) The node voltage method is preferred because it is only one equation with one unknown.

Question (3): [15 Marks]

The load impedance Z_L for the circuit shown in Fig.3 is adjusted until maximum average power is delivered to Z_L . Find Z_L and the maximum average power delivered to Z_L .

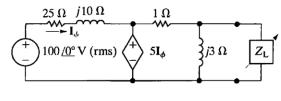
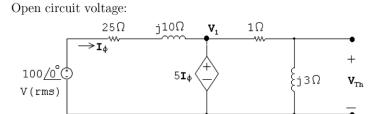


Fig.3

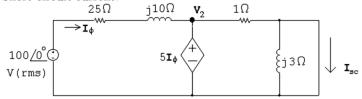


 $\mathbf{V}_1 = 5\mathbf{I}\phi = 5\frac{100 - 5\mathbf{I}_\phi}{25 + j10}$

 $(25+j10)\mathbf{I}_{\phi} = 100 - 5\mathbf{I}\phi$

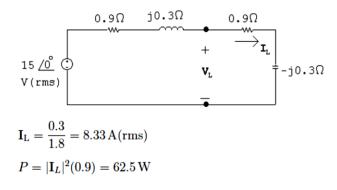
$$\mathbf{I}_{\phi} = \frac{100}{30 + j10} = 3 - j \text{ A}$$
$$\mathbf{V}_{\text{Th}} = \frac{j3}{1 + j3} (5\mathbf{I}_{\phi}) = 15 \text{ V}$$

Short circuit current:



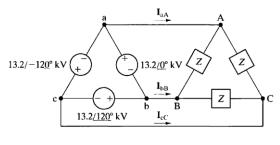
$$\begin{aligned} \mathbf{V}_2 &= 5\mathbf{I}_{\phi} = \frac{100 - 5\mathbf{I}_{\phi}}{25 + j10} \\ \mathbf{I}_{\phi} &= 3 - j1 \, \mathbf{A} \\ \mathbf{I}_{sc} &= \frac{5\mathbf{I}_{\phi}}{1} = 15 - j5 \, \mathbf{A} \\ Z_{\mathrm{Th}} &= \frac{15}{15 - j5} = 0.9 + j0.3 \, \Omega \end{aligned}$$

$$Z_L = Z_{\rm Th}^* = 0.9 - j0.3\,\Omega$$



Question (4): [10 Marks]

The impedance Z in the balanced three-phase circuit in Fig.4 is $100 - j75 \Omega$. Find a) I_{AB} , I_{BC} and I_{CA} , b) I_{aA} , I_{bB} and I_{cC} , c) I_{ba} , I_{cb} , and I_{ac} .





$$\begin{aligned} [\mathbf{a}] \ \mathbf{I}_{AB} &= \frac{13,200/0^{\circ}}{100 - j75} = 105.6/36.87^{\circ} \text{ A (rms)} \\ \mathbf{I}_{BC} &= 105.6/156.87^{\circ} \text{ A (rms)} \\ \mathbf{I}_{CA} &= 105.6/-83.13^{\circ} \text{ A (rms)} \\ [\mathbf{b}] \ \mathbf{I}_{aA} &= \sqrt{3}/-30^{\circ} \mathbf{I}_{AB} = 182.9/66.87^{\circ} \text{ A (rms)} \\ \mathbf{I}_{bB} &= 182.9/-173.13^{\circ} \text{ A (rms)} \\ \mathbf{I}_{cC} &= 182.9/-53.13^{\circ} \text{ A (rms)} \\ \mathbf{I}_{cb} &= \mathbf{I}_{AB} = 105.6/36.87^{\circ} \text{ A (rms)} \\ \mathbf{I}_{cb} &= \mathbf{I}_{BC} = 105.6/156.87^{\circ} \text{ A (rms)} \end{aligned}$$

$$I_{ac} = I_{CA} = 105.6/-83.13^{\circ} A \text{ (rms)}$$

Question (5): [10 Marks]

Two 480 V (rms) loads are connected in parallel. The two loads draw a total average power of 40,800 W at a power factor of 0.8 lagging. One of the loads draws 20 kVA at a power factor of 0.96 leading. What is the power factor of the other load?

 $S_{\rm T} = 40,800 + j30,600 \, {\rm VA}$

 $S_1 = 20,000(0.96 - j0.28) = 19,200 - j5600 \text{ VA}$

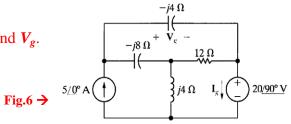
 $S_2 = S_T - S_1 = 21,600 + j36,200 = 42,154.48/59.176^{\circ}$ VA

 $rf = sin(59.176^{\circ}) = 0.8587$

 $pf = cos(59.176^{\circ}) = 0.5124 lagging$

Question (6): [15 Marks]

For the circuit shown in Fig.6, Use the superposition principle to find V_g .



Solution:

Solve two circuits with one source in each of them:

The first circuit with only the current source and replace the voltage source with short circuit. The second circuit with the voltage source after replacing the current source with open circuit.

Question (7): [15 Marks]

In the circuit in Fig.7: Load 1 is a 240 Ω resistor in series with an inductive reactance of 70 Ω ; load 2 is a capacitive reactance of 120 Ω in series with a 160 Ω resistor;

- Find the power factor for load 1 and load 2.
- Find the value of load 3 to get a unity power factor for the whole circuit.

Solve by yourself

Fig.7

Load 1

Load 2

Load 3

With best wishes

Dr. Wael Abdel-Rahman Mohamed